

#### **Dual N-Channel MOSFET**

#### **General Description**

The WSD3044DN33 is the highest performance trench Dual N- Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD3044DN33 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

#### **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
30V	13mΩ	34A

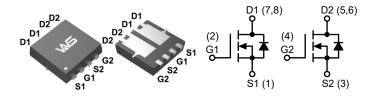
#### Applications

- POL Applications
- MB / VGA / Vcore
- Load Switch
- SMPS 2nd SR

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E<sub>AS</sub> Guaranteed
- Green Device Available

#### **DFN3X3-8L Pin Configuration**



#### **Absolute Maximum Ratings** (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter		Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage		30	V	
V <sub>GS</sub>	Gate-Source Voltage		±20	v	
۱ <sub>D</sub>	Drain Current (Continuous) <sup>1,3</sup>	T <sub>C</sub> =25°C	34		
		T <sub>C</sub> =100°C	15	А	
I <sub>DM</sub>	Drain Current (Pulse) <sup>2</sup>		53		
P <sub>D</sub>	Power Dissipation	ower Dissipation T <sub>C</sub> =25°C		W	
E <sub>AS</sub>	Single Pulse Avalanche Energy		25	mJ	
T <sub>STG</sub>	Storage Temperature Range		-55 to 150	°C	
TJ	Operating Junction Temperature Range		-55 to 150		

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Units	
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient		62	°C/W	
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case		18	C/W	



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#### Electrical Characteristics (T<sub>A</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units		
Static								
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250µA	30			V		
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V			1.0	μA		
I <sub>GSS</sub>	Gate Leakage Current	$V_{DS}$ =0V , $V_{GS}$ =±20V			±100	nA		
On Characte	On Characteristics							
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_{DS}=250\mu A$	1.0	1.5	2.5	V		
D	Drain-Source On-state Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =1A		13	14			
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =1A		19	20	mΩ		
9 <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		33		S		
Switching								
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f = 1.0MHz		3.0		Ω		
Qg	Total Gate Charge			14				
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> =10V , V <sub>DS</sub> =15V , I <sub>D</sub> =5A		5		nC		
Q <sub>gd</sub>	Gate-Drain Charge			4				
T <sub>d(on)</sub>	Turn-On Delay Time			5				
T <sub>r</sub>	Rise Time			9		ns ·		
T <sub>d(off)</sub>	Turn-Off Delay Time	$I_D=1A$ , $R_G=6\Omega$		25				
Τ <sub>f</sub>	Fall Time			8				
Dynamic								
C <sub>iss</sub>	Input Capacitance			650				
C <sub>oss</sub>	Output Capacitance	V <sub>GS</sub> =0V , V <sub>DS</sub> =15V , f = 1.0MHz		115		pF		
C <sub>rss</sub>	Reverse Transfer Capacitance	7		100				
Drain-Source	Drain-Source Diode Characteristics and Maximum Ratings							
I <sub>S</sub>	Continuous Source Current				18			
I <sub>SM</sub>	Pulsed Source Curren <sup>3</sup>	− V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			55	A		
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> =1A , V <sub>GS</sub> =0V			1.2	V		

Note:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.

2.  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =16A,  $R_{G}$ =25, Starting  $T_{J}$ =25°C.

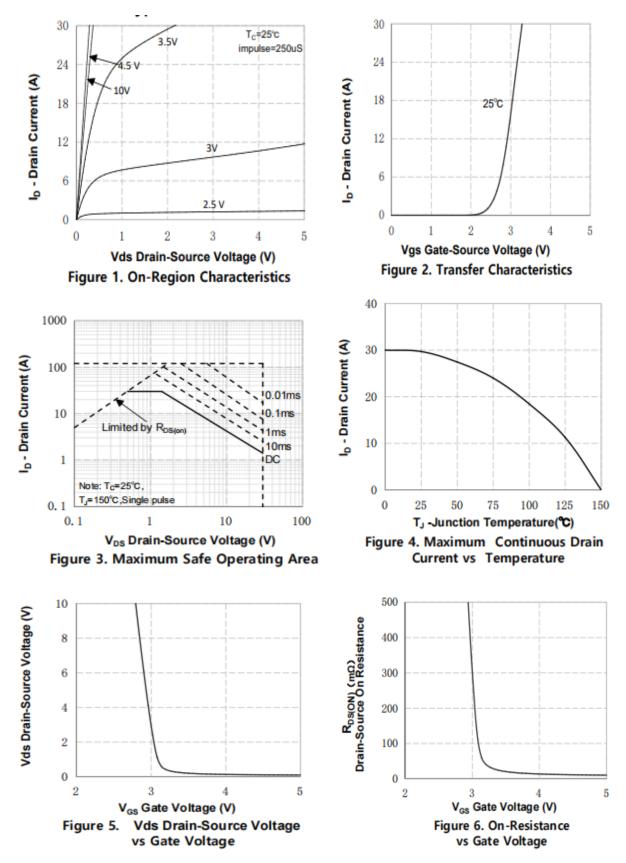
3. The data tested by pulsed , pulse width  $\leq$  300µs , duty cycle  $\leq$  2%.

4. Essentially independent of operating temperature.



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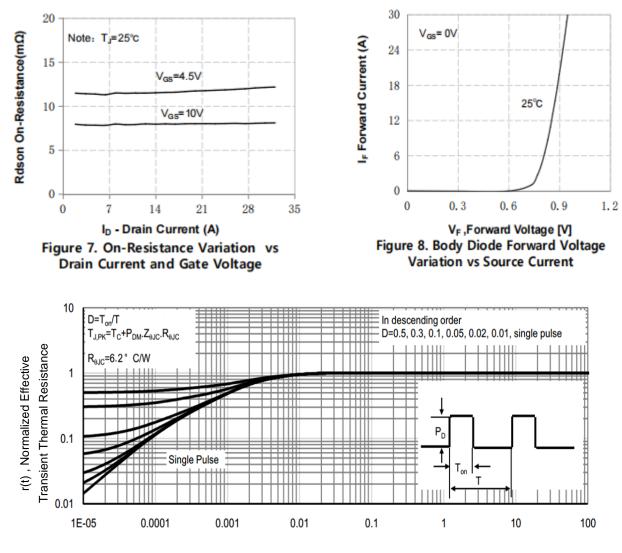
#### **Typical Characteristics**





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### **Typical Characteristics (Cont.)**



T1, Square Wave Pulse Duration(sec) Fig9. T1, Transient Thermal Response Curve

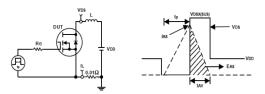


Fig10. Unclamped Inductive Test Circuit and waveforms

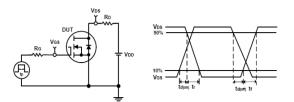
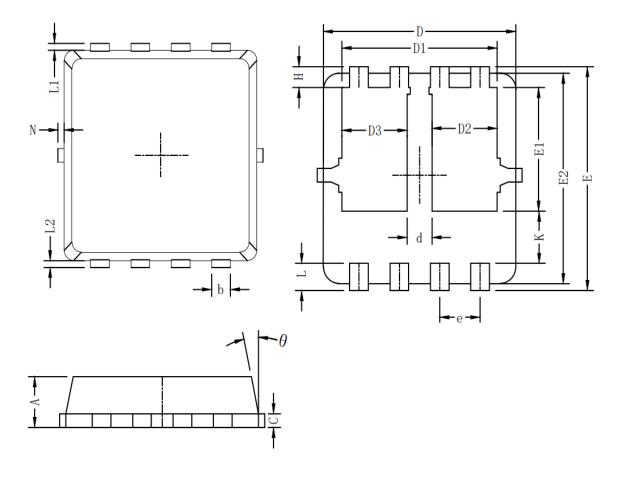


Fig11. Switching Time Test Circuit and waveforms



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### **Packaging information**



Symbol	Dim in mm			
Symbol	min	typ	max	
А	0.6	0.75	0.9	
b	0.2	0.3	0.4	
С	0.15	0.2	0.25	
D	3	3.1	3.2	
D1	2.3	2.45	2.6	
D2/D3	0.8	1	1.2	
E	3.15	3.3	3.45	
E1	1.43	1.73	1.93	
E2	2.9	3.05	3.2	
е	0.65BSC			
Н	0.2	0.35 0.5		
К	0.57	0.77	0.87	
L	0.3	0.4	0.5	
L1/L2	0.1REF			
θ	8°	10° 13°		
Ν	0		0.15	
d	0.3	0.4	0.5	



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